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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/633,055	07/31/2003	Shahriar Ahmed	42P10970C	3607

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EXAMINER

IM, JUNGHWA M

ART UNIT	PAPER NUMBER
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2811

DATE MAILED: 12/15/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

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Office Action Summary	Application No. 10/633,055	Applicant(s) AHMED ET AL.	
	Examiner Junghwa M. Im	Art Unit 2811	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 03 October 2005.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 13, 17-22 and 27-31 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☐ Claim(s) _____ is/are rejected.
- 7) ☒ Claim(s) 13, 17-22 and 27-31 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 13, 17-22 and 27-31 are rejected under 35 U.S.C. 103(a) as being unpatentable over Akbar et al. (U.S. Pat. No. 4957875), hereinafter Akbar in view of Eklund (U.S. Pat. No. 5087580).

Regarding claim 13, Fig. 8 of Akbar shows a bipolar junction transistor comprising:
in a substrate 32, a first isolation structure 36 spaced apart from a second isolation structure 38;

an emitter stack 16 disposed above the substrate and between the first isolation structure and the second isolation structure; and

a recess (a portion between the regions 17, 18) disposed immediately adjacent to the emitter stack and disposed between the emitter stack and the first isolation structure, wherein the recess exposes a collector tap 26, wherein the emitter stack and the recess share a boundary.

Fig. 8 of Akbar shows most aspect of the instant invention except "an emitter cut provide at the bottom of said emitter stack and on top of an intrinsic base structure formed in the substrate." Fig. 1 of Eklund shows a semiconductor device wherein an emitter cut (a bottom portion 61 of the emitter 60) provide at the bottom of said emitter stack and on top of an

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intrinsic base structure (a portion labeled 61) formed in the substrate. It would have been obvious to one of ordinary skill in the art at the time of the invention to incorporate the teachings of Eklund into the device of Akbar in order to have an emitter cut provided at the bottom of said emitter stack and on top of an intrinsic base structure in the substrate to reduce parasite capacitance between the emitter and the extrinsic base.

Regarding claim 17, Fig. 8 of Akbar shows a buried layer 34 in the substrate between the first isolation structure and the second isolation structure.

Regarding claim 18, Fig. 8 of Akbar shows a bipolar junction transistor further including: in a substrate, an epitaxial base layer 14 disposed below the emitter stack; a collector structure 12 disposed in the substrate below the emitter stack; and an intrinsic base structure 22 disposed between the emitter stack and the collector structure.

Regarding claim 19, Fig. 8 of Akbar shows a bipolar junction transistor further including: in the substrate, an epitaxial base layer 14 disposed below the emitter stack; a collector structure 12 disposed in the substrate below the emitter stack; and a dielectric layer 17 disposed above the substrate and below the emitter stack and above the collector structure; and

an intrinsic base structure 22 disposed between the emitter stack and the collector structure.

Fig. 8 of Akbar shows most aspect of the instant invention except “the dielectric layer is patterned for said emitter cut to be formed therein.” Fig. 1 of Eklund shows a semiconductor device wherein the dielectric layer (44, 50, 74) is patterned for the emitter cut to be formed

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therein. It would have been obvious to one of ordinary skill in the art at the time of the invention to incorporate the teachings of Eklund into the device of Akbar in order to have to the dielectric layer patterned for the emitter cut to be formed therein to reduce parasite capacitance between the emitter and the extrinsic base.

Note that "epitaxial" is a process designation, and would not carry patentable weight in this claim drawn to a product. See *In re Thorp*, 227 USPQ 964 (Fed. Cir. 1985).

Regarding claim 20, Fig. 8 of Akbar shows a collector tap 26 is N type.

Regarding claim 21, Fig. 1 of Eklund shows that the substrate includes a BiCMOS structure (col. 3, lines 52-55).

Regarding claim 22, Fig. 8 of Akbar shows the BJT is selected from a monojunction BJT device and a heterojunction BJT device.

Regarding claim 27, Fig. 8 of Akbar shows the collector tap 127 is self-aligned with the emitter stack.

Also, note that "self-aligned" is a process designation and would thus not carry patentable weight in this claim drawn to a product. See *In re Thorp*, 227 USPQ 964 (Fed. Cir. 1985).

Regarding claim 28, Fig. 8 of Akbar shows the bipolar junction transistor is an NPN transistor, and the collector tap is N type.

Regarding claim 29, Akbar discloses the bipolar junction transistor is a PNP transistor, and the collector tap is P type (col. 3, lines 24-25).

Regarding claim 30, Fig. 8 of Akbar shows the collector tap has no doping that is different from the substrate.

Regarding claim 31, Fig. 8 of Akbar shows the recess is a contact corridor.

Response to Arguments

Applicant's arguments with respect to pending claims have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a).

Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

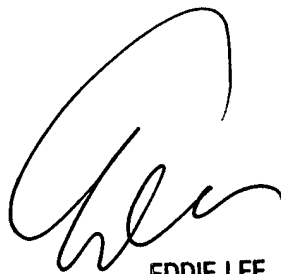
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Junghwa M. Im whose telephone number is (571) 272-1655. The examiner can normally be reached on MON.-FRI. 8:30AM-5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's acting supervisor, Eddie C. Lee can be reached on (571) 272-1732. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

jmi



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